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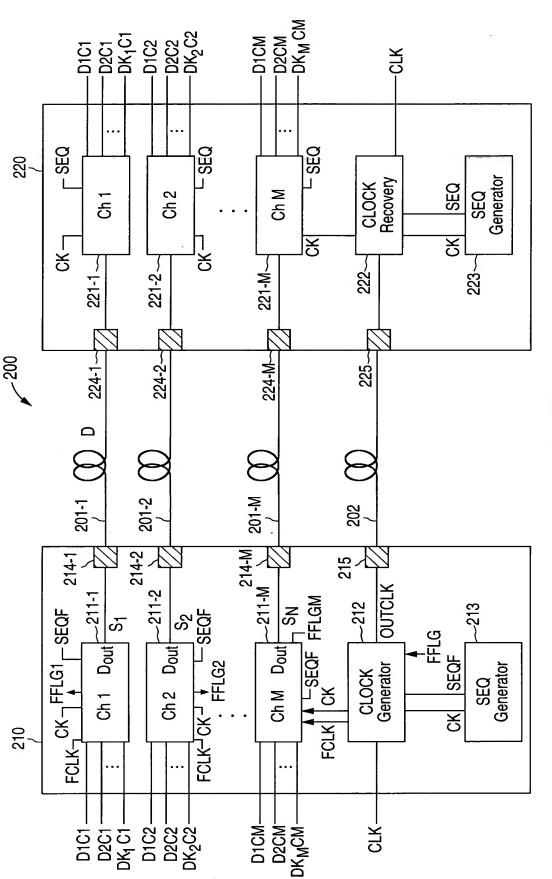
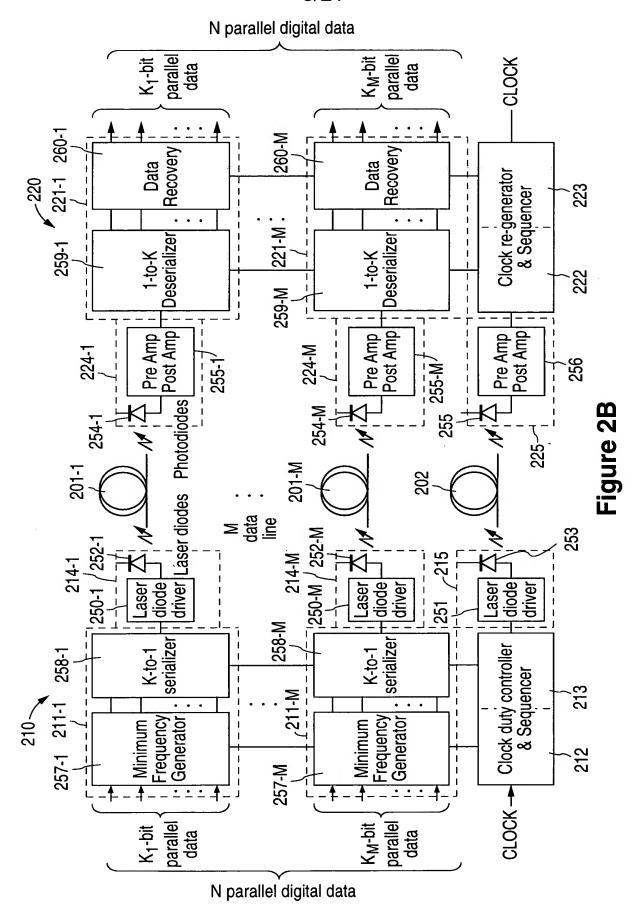
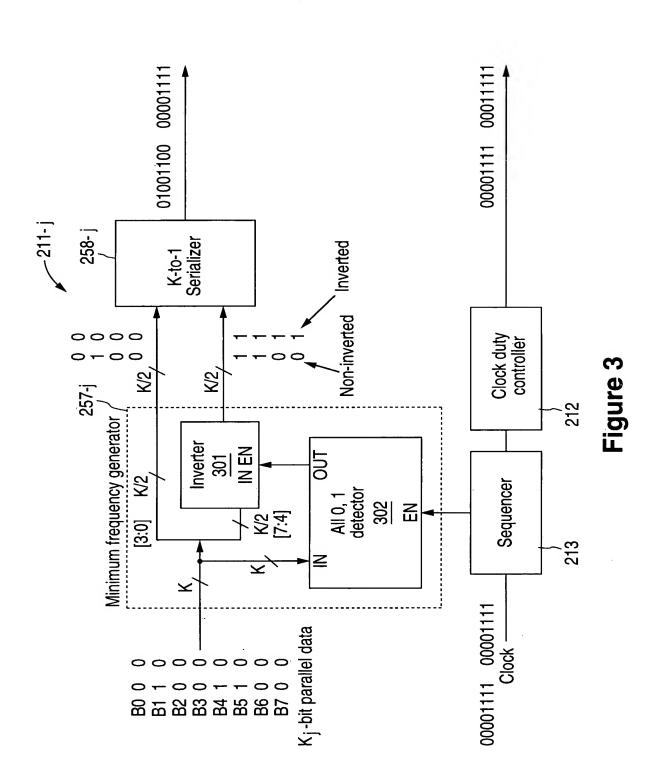


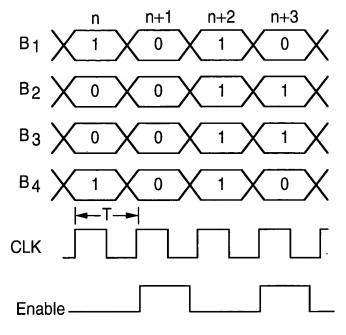
Figure 2A

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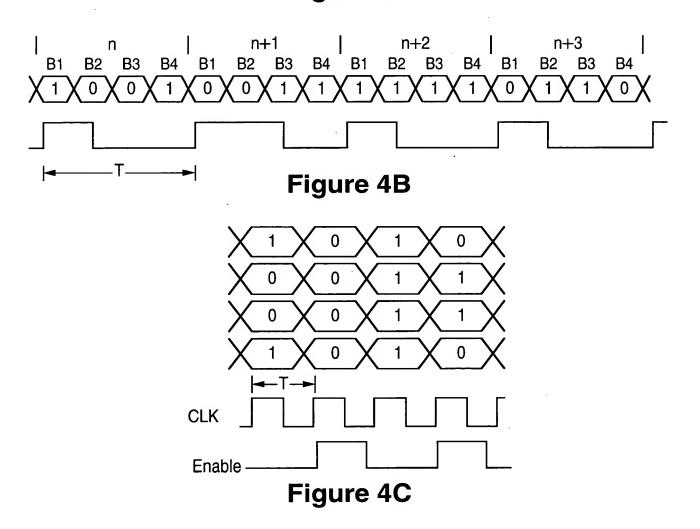




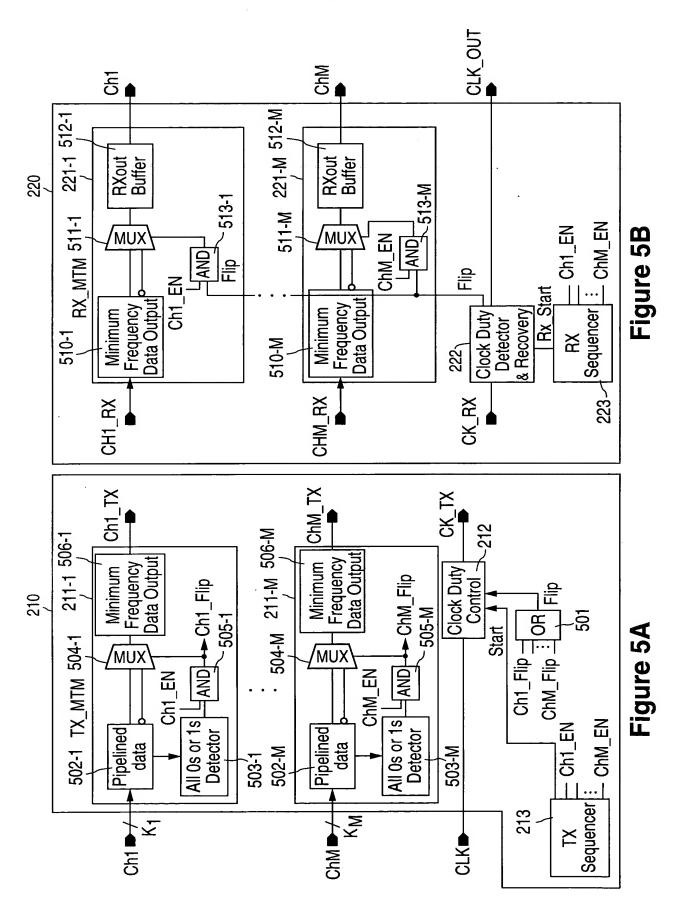
DC Balancing Scheme On Serial Data On-The-Fly Shin, Peter Docket No.: M-11461 US



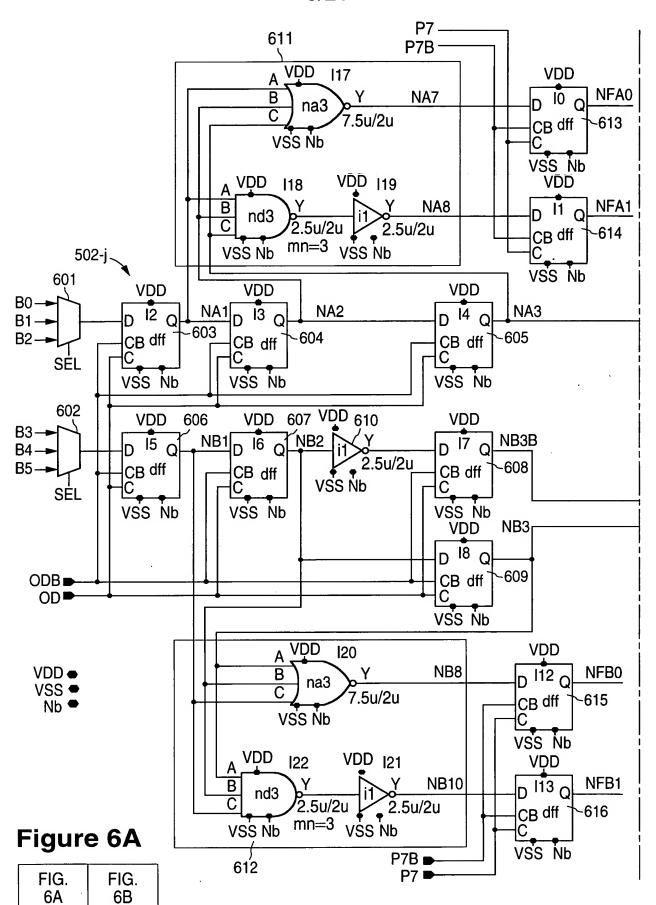
# Figure 4A



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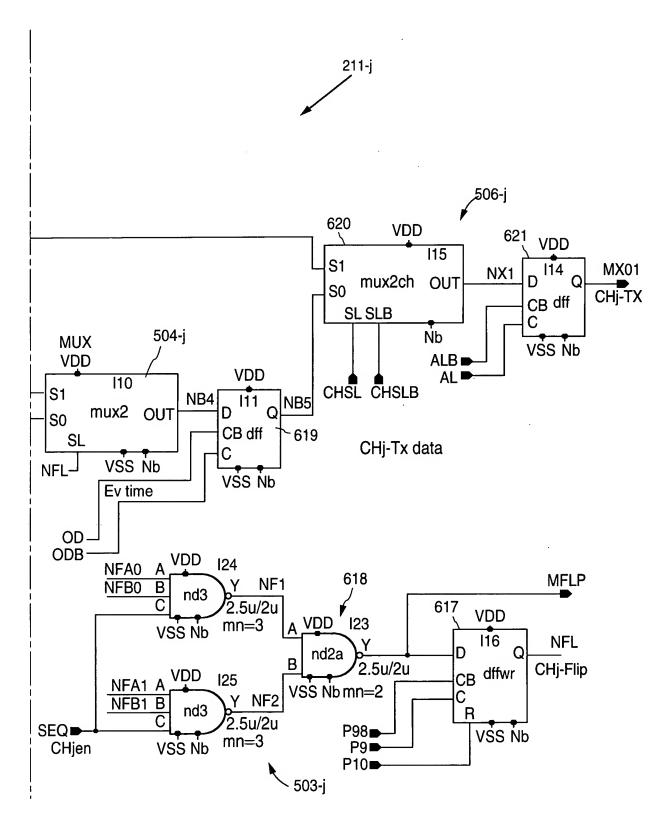
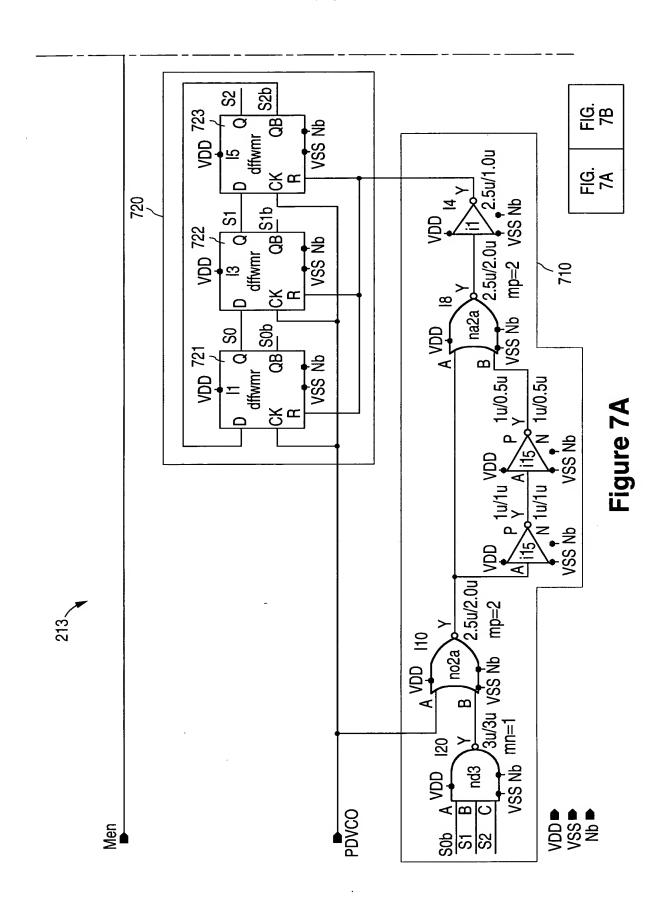


Figure 6B



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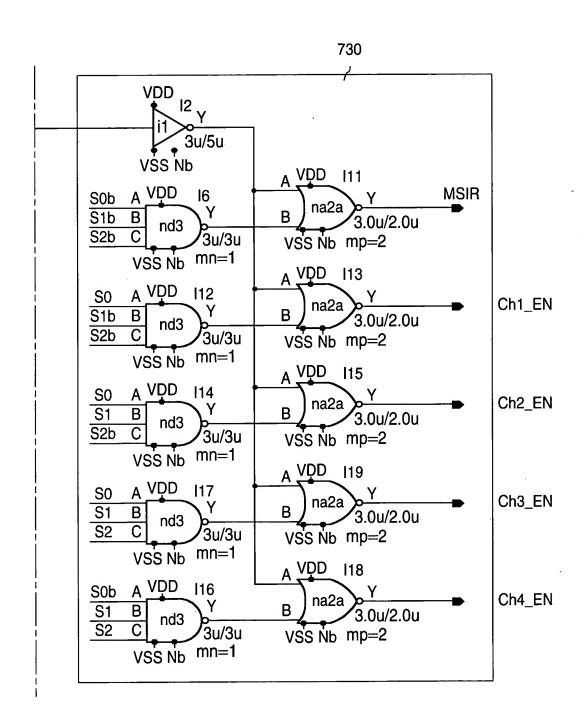
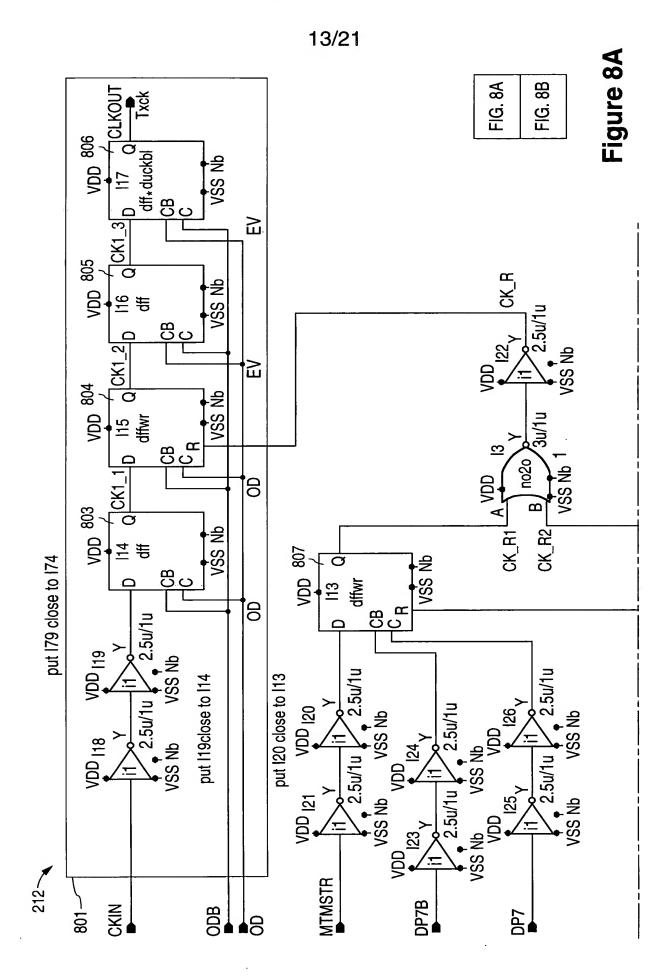
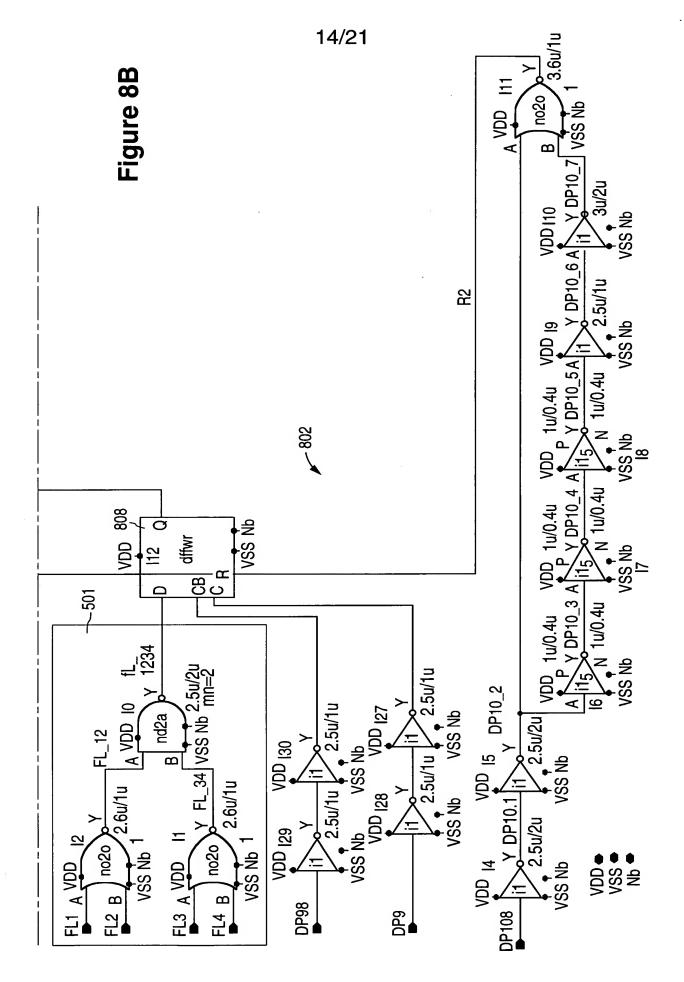


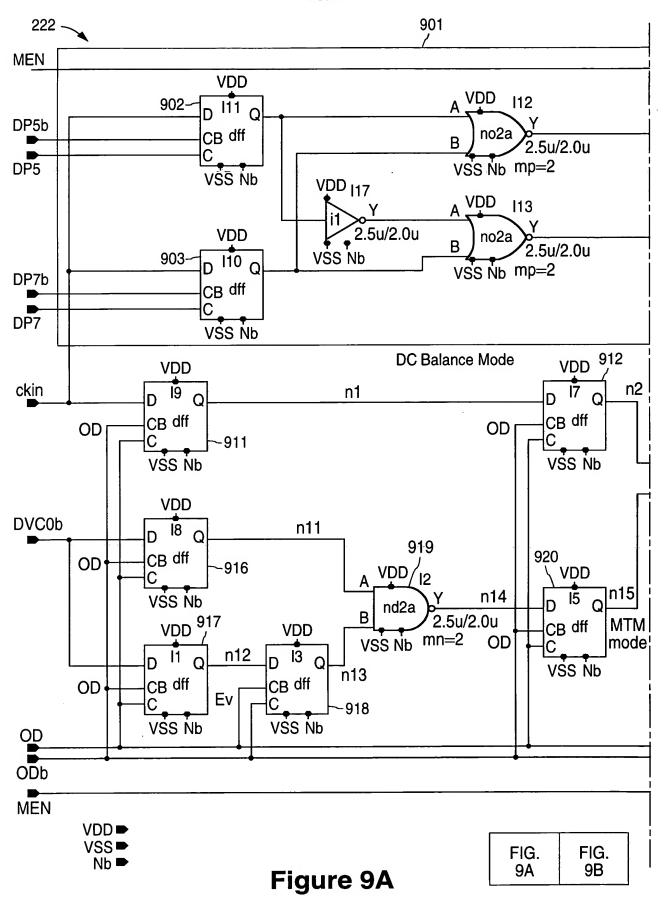
Figure 7B



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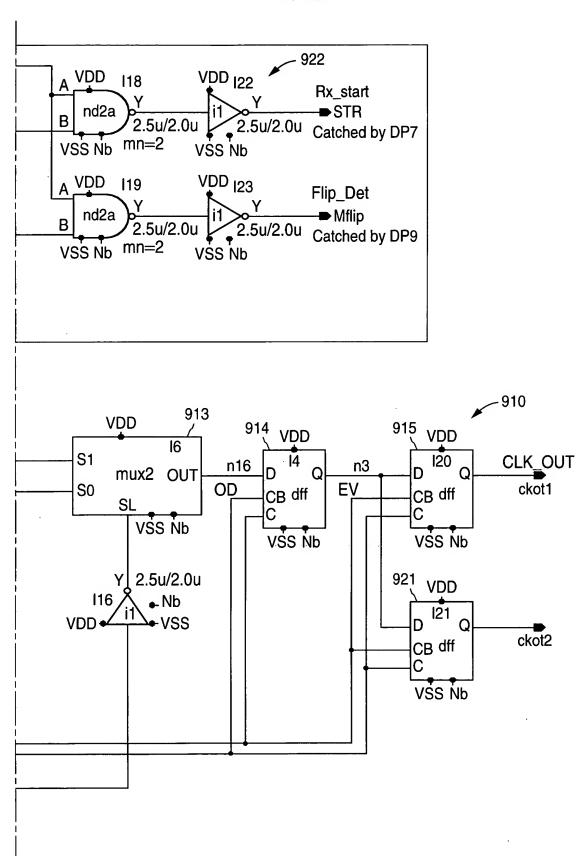
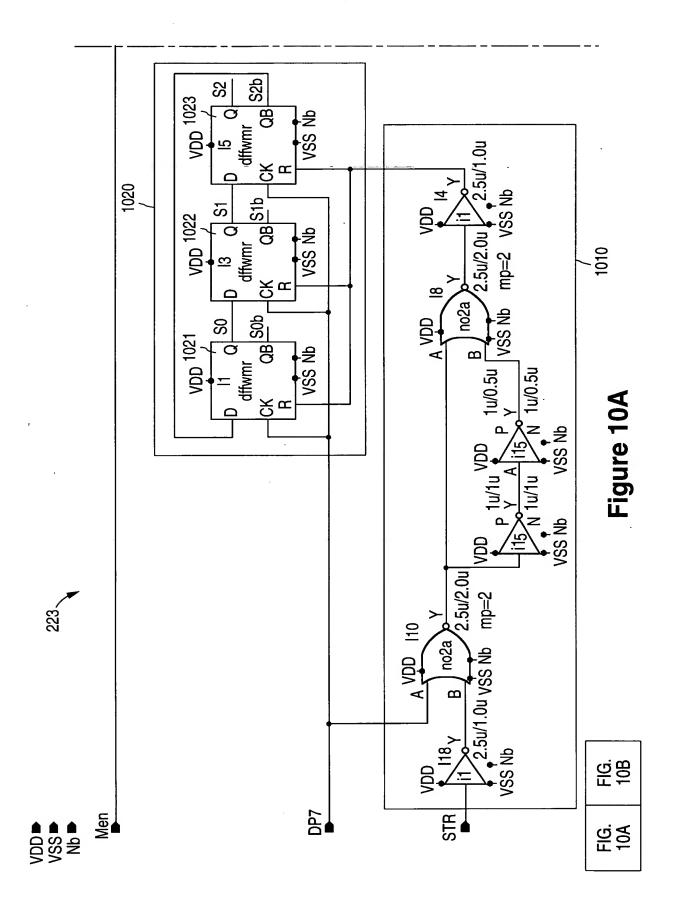


Figure 9B

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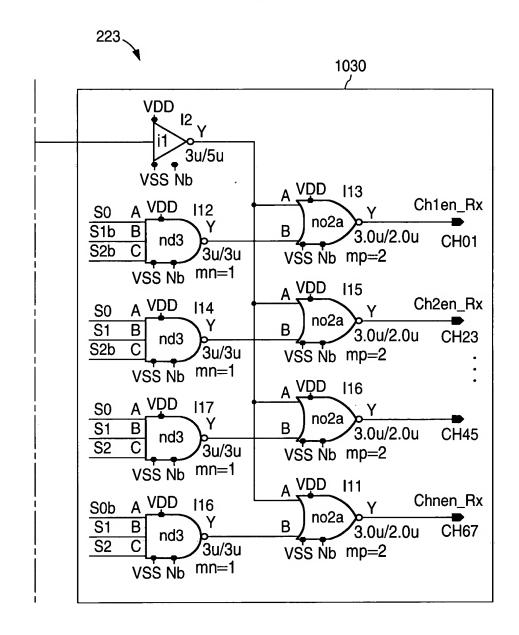
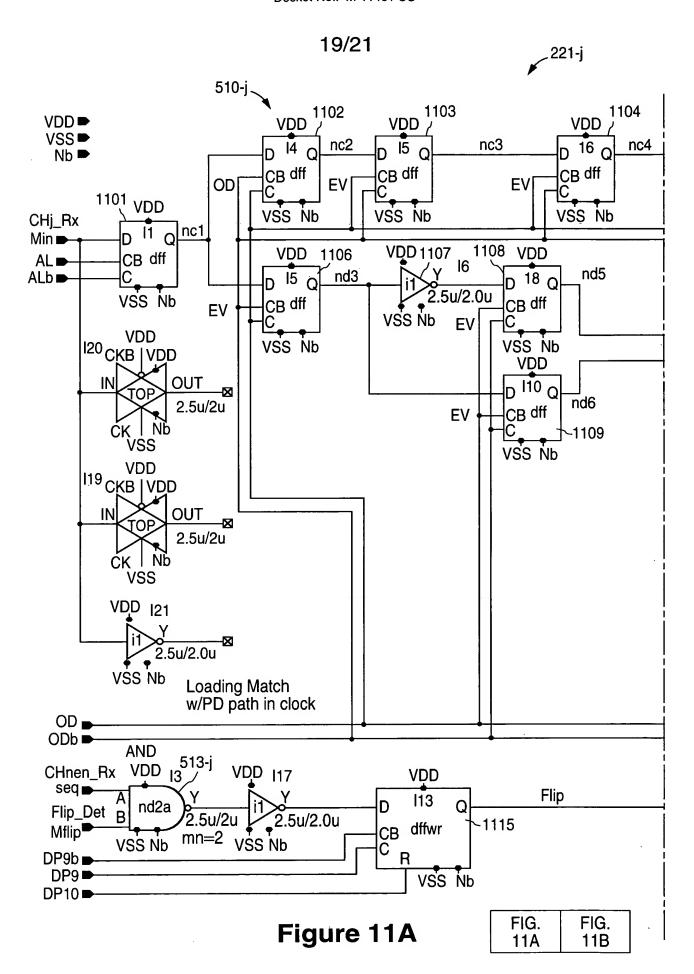


Figure 10B



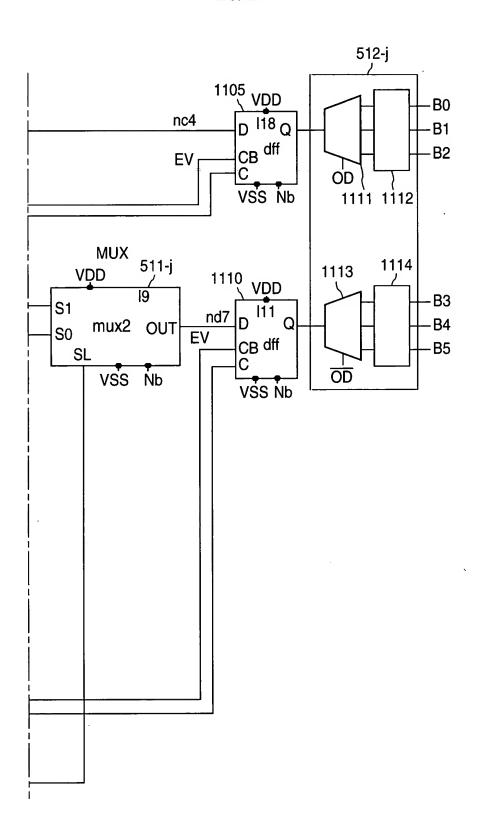


Figure 11B

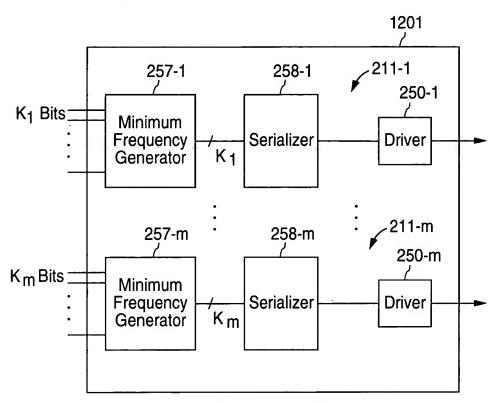


Figure 12A

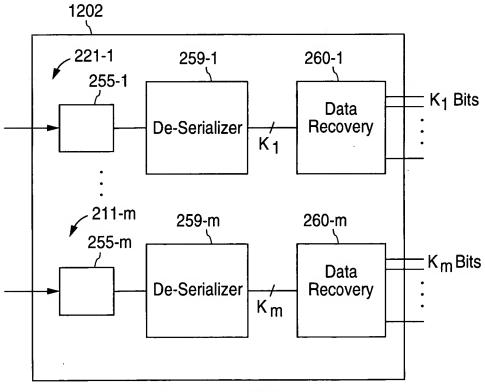


Figure 12B